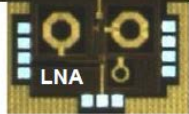
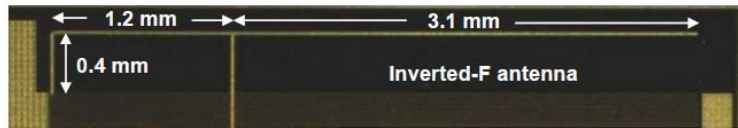
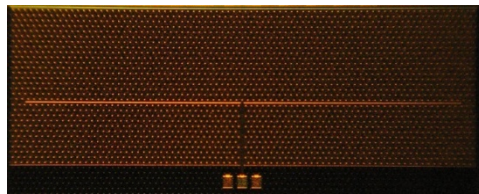


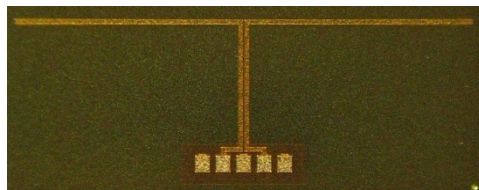
シリコン基板上のアンテナの課題



inverted-F/LNA

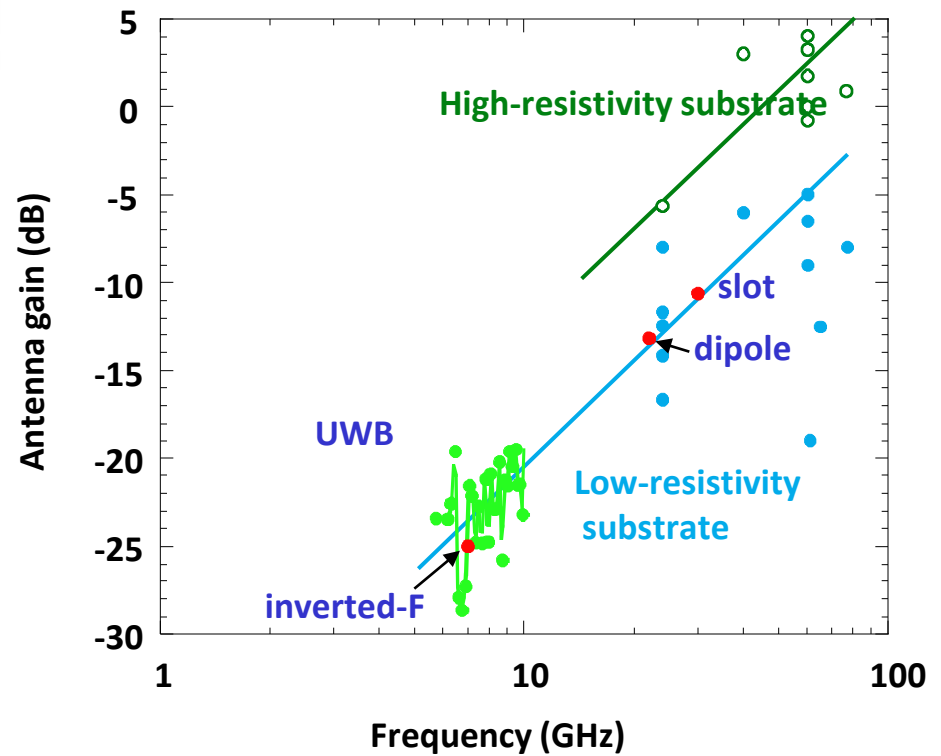


slot

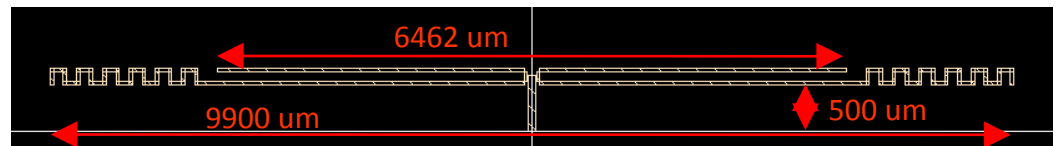


dipole

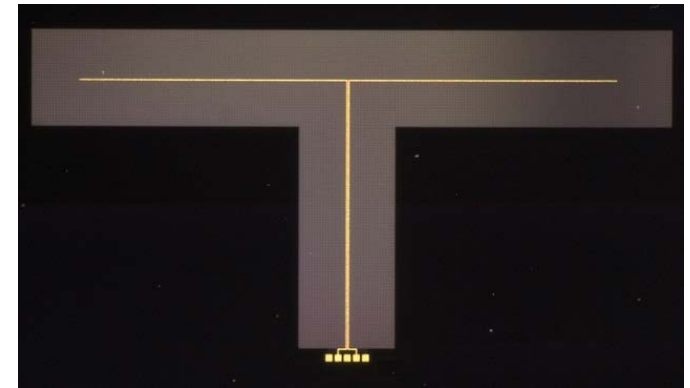
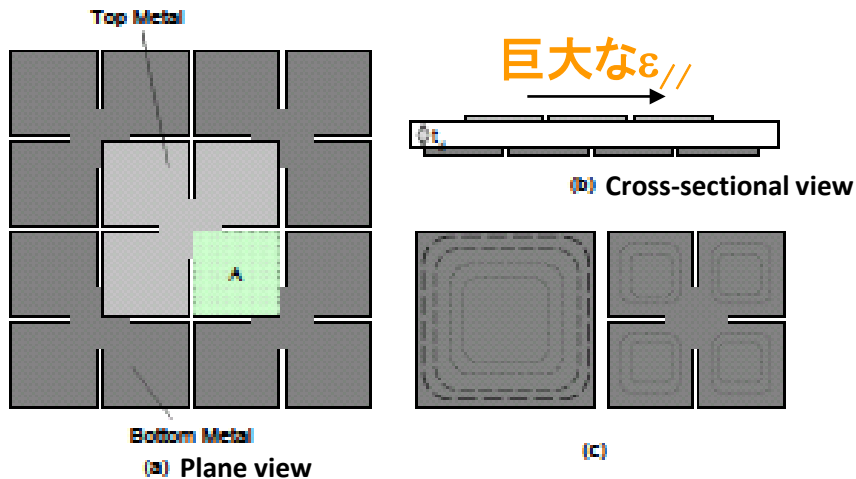
CMOSに用いられる低抵抗基板の上に高利得アンテナを実現できない。



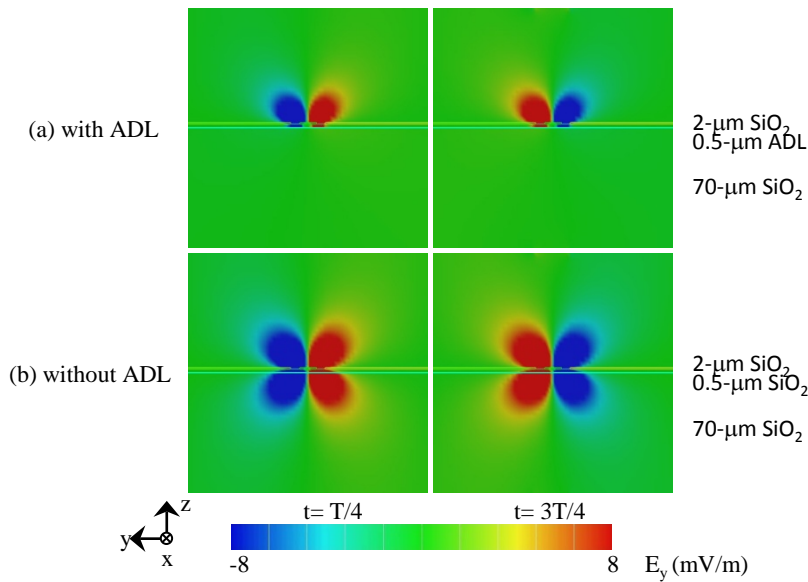
UWB



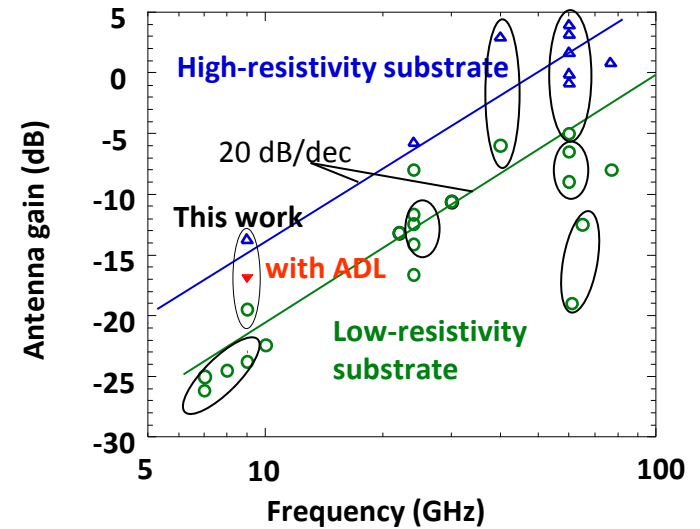
人工誘電体(ADL)導入によるアンテナの高利得化



チップ写真



人工誘電体



アンテナの高利得化